

Thyristor-Thyristor Module, 122 Amps

Features

- Improved glass passivation for high reliability
- Exceptional stability at high temperatures
- High di/dt and dv/dt capabilities
- Low thermal resistance



Type number	Voltage Code	V _{RRM} , Maximum repetitive peak reverse voltage (V)	V _{RSM} , Maximum non-repetitive peak reverse voltage (V)	V _{DRM} , Maximum repetitive peak off-state voltage (V)	I _{RRM} , Maximum reverse leakage current @ T _{JMAX} (mA)
NTT122	60	600	700	600	max. 35
	80	800	900	800	
	100	1000	1100	1000	
	120	1200	1300	1200	
	140	1400	1500	1400	
	160	1600	1700	1600	
	180	1800	1900	1800	

Electrical Characteristics (T_A = 25°C unless otherwise noted)

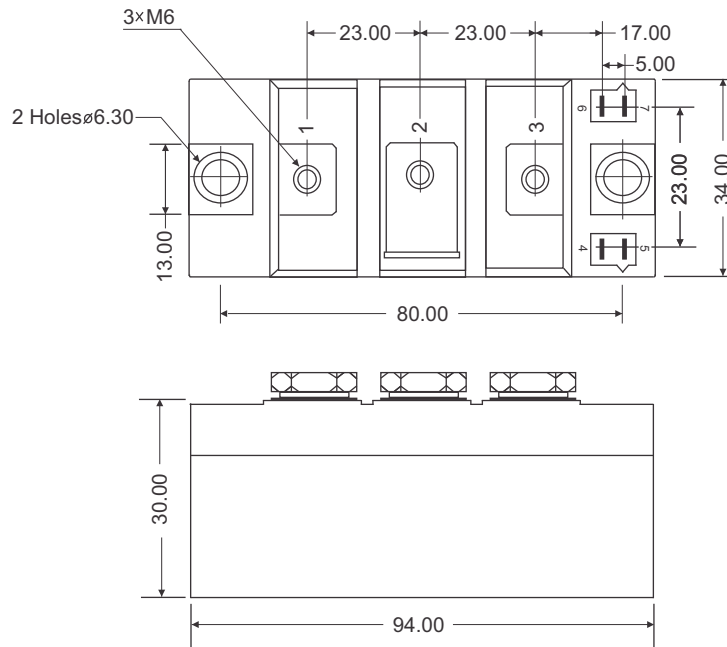
Parameter	Symbol	Values	Units
Maximum average forward current @ T _J = 85°C	I _{T(AV)}	122	A
Maximum average RMS forward current	I _{T(RMS)}	191	A
Maximum non-repetitive surge current	I _{TSM}	3600	A
Maximum I ² t for fusing	I ² t	64800	A ² s
Forward voltage drop	V _{TM}	max. 1.5	V
Critical rate of rise of on-state current	di/dt	max. 200	A/μs
Critical rate of rise of off-state voltage	dv/dt	max. 1000	V/μs
Gate current required to trigger	I _{GT}	150	mA
Gate voltage required to trigger	V _{GT}	3	V
Maximum holding current	I _H	100	mA
Maximum latching current	I _L	200	mA
Isolation voltage, 1 min.	V _{ISO}	3000	V

Thermal & Mechanical Specifications (T_A = 25°C unless otherwise noted)

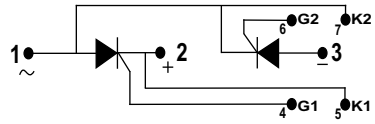
Parameter	Symbol	Values	Units
Operating junction temperature range	T _J	-40 to +125	°C
Storage temperature	T _{stg}	-40 to +125	°C
Thermal resistance, junction to case	R _{th(jc)}	0.21	°C/W
Mounting torque	to heatsink	F	Nm
	to terminals		
Weight	W	220	g

Package Outline

(All dimensions in mm)



Circuit Configuration

Circuit Description	Configuration Code	Circuit Drawing
Series Connection (doubler circuit)	N	 The circuit drawing shows a series connection of two thyristors. The first thyristor has terminals 1 (anode), 2 (cathode), and 3 (gate). The second thyristor has terminals 4 (anode), 5 (cathode), and 6 (gate). The circuit is connected in a doubler configuration: terminal 1 is connected to terminal 4, terminal 2 is connected to terminal 6, and terminal 3 is connected to terminal 5. The output is taken from terminal 6 (G2) and terminal 7 (K2).

Ordering Table

NTT	122	N	160
1	2	3	4

1 – Power Module

- > NDD = Diode-Diode
- > NTD = Thyristor-Diode
- > NTT = Thyristor-Thyristor

2 – Current Rating = $I_{T(AV)}$

3 – Circuit Configuration (see Table)

4 – Voltage Code (see Voltage Ratings table)