

Thyristor-Thyristor Module, 106 Amps

Features

- Improved glass passivation for high reliability
- Exceptional stability at high temperatures
- High di/dt and dv/dt capabilities
- Low thermal resistance

**Voltage Ratings** ($T_A = 25^\circ\text{C}$, unless otherwise noted)

Type number	Voltage Code	V_{RRM} , Maximum repetitive peak reverse voltage	V_{RSM} , Maximum non-repetitive peak reverse voltage	V_{DRM} , Maximum repetitive peak off-state voltage	I_{RRM} , Maximum reverse leakage current @ T_{JMAX}
		(V)	(V)	(V)	(mA)
NTT106 H	200	2000	2100	2000	max. 30
	220	2200	2300	2200	

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

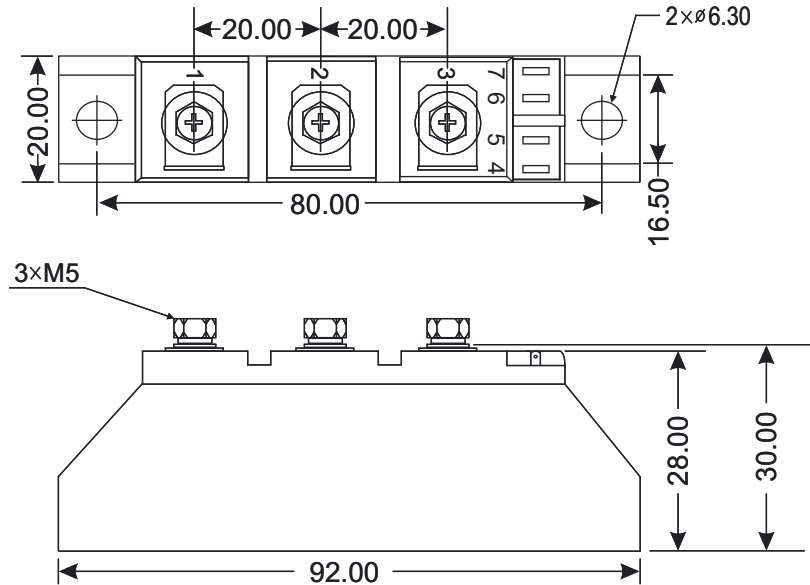
Parameter	Symbol	Values	Units
Maximum average forward current @ $T_J = 85^\circ\text{C}$	$I_{T(AV)}$	106	A
Maximum average RMS forward current	$I_{T(RMS)}$	165	A
Maximum non-repetitive surge current	I_{TSM}	2200	A
Maximum I^2t for fusing	I^2t	24200	A^2s
Forward voltage drop	V_{TM}	max. 1.6	V
Critical rate of rise of on-state current	di/dt	max. 150	$\text{A}/\mu\text{s}$
Critical rate of rise of off-state voltage	dv/dt	max. 1000	$\text{V}/\mu\text{s}$
Gate current required to trigger	I_{GT}	min. 150	mA
Gate voltage required to trigger	V_{GT}	min. 3	V
Maximum holding current	I_H	150	mA
Maximum latching current	I_L	200	mA
Isolation voltage, 1 min.	V_{ISO}	4000	V

Thermal & Mechanical Specifications ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Values	Units
Operating junction temperature range	T_J	-40 to +125	$^\circ\text{C}$
Storage temperature	T_{stg}	-40 to +125	$^\circ\text{C}$
Thermal resistance, junction to case	$R_{th(jc)}$	0.29	$^\circ\text{C}/\text{W}$
Mounting torque	F	to heatsink	$5 \pm 15\%$
		to terminals	$3 \pm 15\%$
Weight	W	100	g

Package Outline

(All dimensions in mm)



Circuit Configuration

Circuit Description	Configuration Code	Circuit Drawing
Series Connection (doubler circuit)	N	<p>The circuit drawing shows a series connection of two thyristors. Terminal 1 is the AC input. Terminal 2 is the gate of the first thyristor (K1). Terminal 3 is the cathode of the first thyristor and the gate of the second thyristor (K2). Terminal 4 is the cathode of the second thyristor. Terminals 5 and 6 are the gates (G1 and G2) of the thyristors. Terminal 7 is the AC output.</p>

Ordering Table

NTT	106	N	200	H
1	2	3	4	5

1 – Power Module

- > DD = Diode-Diode
- > TD = Thyristor-Diode
- > TT = Thyristor-Thyristor

2 – Current Rating = $I_T(AV)$

3 – Circuit Configuration (see Table)

4 – Voltage Code (see Voltage Ratings table)

5 – High Voltage ($\geq 2000V$)